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PATENT

Serial No. 10/509,478

Amendment in Reply to Office Action mailed on January 10, 2006

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A method of embedding an additional layer of error correction into an error correcting code, wherein information is encoded into code words of said code and wherein a number of code words are arranged in the columns of a code block, said method comprising the ~~steps~~ acts of:

[[-]] reducing the length of each row of said code block by adding X row symbols together to form Y row symbols replacing the X row symbols, Y being less than X, to form shortened rows according to a predetermined adding rule resulting in a reduced code block,

[[-]] encoding the shortened rows of said reduced code block using a horizontal error correcting code to obtain horizontal parities, and

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[[-]] embedding said horizontal parities as additional layer in said error correcting code.

2. (Currently Amended) A The method as claimed in claim 1, wherein said code block is a Long Distance Code (LDC) block comprising LDC code words, in particular Reed Solomon code words over $GF(2^8)$, arranged in the columns of said LDC block.

3. (Currently Amended) A The method as claimed in claim 2, further comprising the initial steps acts of:

[[-]] reducing the length of each LDC code word of said LDC block resulting in a first intermediate block, and

[[-]] extending each row of said first intermediate block by a predetermined number of symbols having predetermined values resulting in a second intermediate block which is used as code block in subsequent steps acts.

4. (Currently Amended) A The method as claimed in claim 3, wherein the length of each LDC code word is reduced by puncturing a predetermined number of symbols at predetermined positions of each

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LDC code word.

5. (Currently Amended) A The method as claimed in claim 3, wherein the length of each LDC code word is reduced by selecting a predetermined number of symbols of a LDC code word and by determining a predetermined number of parity symbols for said LDC code word.

6. (Currently Amended) A The method as claimed in claim 3, wherein the rows of said first intermediate block are extended by adding a predetermined number of symbols having symbol value zero to each row.

7. (Currently Amended) A The method as claimed in claim 1, wherein the length of each row of said code block is reduced by adding the $(m+(j+i \bmod k))$ -th symbol of each row to the j -th symbol of the same row, i being the row number, j being the column number and m being the maximum number of columns divided by 2.

8. (Currently Amended) A The method as claimed in claim 1,

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wherein said code block comprises two blocks of a product code, in particular of a DVD product code, comprising product code words, in particular product code words of the same DVD product code such as code words over $GF(2^8)$, arranged in the columns of said code block.

9. (Currently Amended) A The method as claimed in claim 1, wherein said horizontal parities are encoded by an additional error correcting code, in particular by a Burst Indicator Subcode (BIS) comprising Reed Solomon code words over $GF(2^8)$ or by an additional parity code comprising Reed Solomon code words over $GF(2^8)$.

10. (Currently Amended) A method of decoding an error correcting code into which an additional layer of error correction is embedded according to a method of claim 1, wherein horizontal parities are embedded as additional layer in said error correcting code and wherein a number of code words of said code are arranged in the columns of a code block, said method comprising the steps acts of:

[[-]] extracting said horizontal parities from said error correcting code,

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[[-]] reducing the length of each row of said code block by adding row symbols together identical to the method according to said predetermined adding rule used during the method of encoding according to claim 1 resulting in a reduced code block, and

[[-]] decoding the shortened rows of said reduced code block using said horizontal parities.

11. (Currently Amended) A ~~The~~ method as claimed in claim 10, further comprising the ~~steps~~ acts of decoding a received data stream to obtain said code block and of correcting errors and erasures in the obtained code block by subtracting row symbols of said obtained code block from row symbols of said reduced code block according to a predetermined subtraction rule reverse to said predetermined adding rule.

12. (Currently Amended) ~~Apparatus~~ An apparatus for embedding an additional layer of error correction into an error correcting code, wherein information is encoded into code words of said code and wherein a number of code words are arranged in the columns of a code block, comprising:

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[[-]] means for reducing the length of each row of said code block by adding X row symbols together to form Y row symbols replacing the X row symbols, Y being less than X, to form shortened rows according to a predetermined adding rule resulting in a reduced code block,

[[-]] means for encoding the shortened rows of said reduced code block using a horizontal error correcting code to obtain horizontal parities, and

[[-]] means for embedding said horizontal parities as additional layer in said error correcting code.

13. (Currently Amended) ~~Apparatus~~ The apparatus for decoding an error correcting code into which an additional layer of error correction is embedded according to a method of claim 1, wherein horizontal parities are embedded as additional layer in said error correcting code and wherein a number of code words of said code are arranged in the columns of a code block, comprising:

[[-]] means for extracting said horizontal parities from said error correcting code,

[[-]] means for reducing the length of each row of said

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code block by adding row symbols together identical to the method according to said predetermined adding rule used during the method of encoding according to claim 1 resulting in a reduced code block,

and

[[-]] means decoding the shortened rows of said reduced code block using said horizontal parities.

14. (Currently Amended) ~~Storage~~ A storage medium storing data in form of code words of an error correcting code into which an additional layer of error correction is embedded according to a method of claim 1, wherein horizontal parities are embedded as additional layer in said error correcting code and wherein a number of code words of said code are arranged in the columns of a code block.

15. (Currently Amended) ~~Signal~~ A signal comprising data in form of code words of an error correcting code into which an additional layer of error correction is embedded according to a method of claim 1, wherein horizontal parities are embedded as additional layer in said error correcting code and wherein a number

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of code words of said code are arranged in the columns of a code block.

16. (Currently Amended) ~~Computer~~ A computer program comprising program code means for causing a computer to implement the ~~steps~~ acts of the method of claim 1 when said program is run on a computer.

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